

SRI VENKATESWARA COLLEGE OF ENGINEERING

COURSE DELIVERY PLAN - LABORATORY

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Department of Information Technology

LP: CS18561

B.E/B.Tech/M.E/M.Tech: EEE

Regulation::2018A

Rev. No: 02

PG Specialisation

· NA

Date: 10/7/2023

Sub. Code / Sub. Name: CS18561- Programming and Data Structures Laboratory

Session No*	List of Experiments	CO	
1	Classes and Objects i. Constructors and Destructors ii. Static data member and member Function.		
2	Friend Function Dynamic Memory Allocation		
3	Compile Time Polymorphism i. Function overloading ii. Operator Overloading		
4	Inheritance	3	
5	Run-time Polymorphism i. Virtual Functions ii. Abstract Base classes		
6	Linked Lists - Implementation of Singly Linked List Addition of two polynomial expressions		
7 .	Stacks i. Implementation of Stack operations using Arrays ii. Implementation of Stack operations using Linked List		
8	Applications of stack i. Infix-to postfix conversion ii. Expression Evaluation		
9	Queue Implementation of Circular Queue operations using Arrays		
10	Implement Binary Search Tree operations - Creation, insertion, delete, search, Traversals	2	
- 11 -	Implement graph traversals	2	
12	Implement Linear and Binary Search algorithms	5	
13	Implement Merge Sort and Quick Sort	5	
14	Model Exam		

* Session Duration: 150 minutes

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	Prepared by	Approved by
Signature	Damp 10/1/23	· Contra
Name	Dr. A. Indumathi, Ms. N. Uma	Dr. V. Vidhya
Designation	Associate Professor/INT Assistant Professor/INT	HOD/INT
Date	10/07/2023	10/07/2023
Remarks* :		
Remarks* :		
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^{*} If the same lab plan is followed in the subsequent semester/year it should be mentioned and signed by the Faculty and the HOD