SRI VENKATESWARA COLLEGE OF ENGINEERING

Department of Information Technology	LP: IT22411
	Rev. No: 01
B.E/B.Tech/M.E/M.Tech : B.Tech　　　　Regulation: 2022	Date:21.1.25
Sub. Code / Sub. Name:IT22411/ Microprocessor and Microcontroller Interfacing Laboratory	
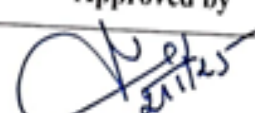
Session No*	List of Experiments
	CYCLE-I
1	**8086 Programs using kits and MASM** 　i)　Basic arithmetic and Logical operations 　ii)　Move a data block without overlap
2	i)　Code conversion and decimal arithmetic 　ii)　Matrix operations
3	i)　Floating point operations 　ii)　String manipulations
4	Sorting and Searching
5	i)　Password checking 　ii)　Print RAM size and System Date
6	Counters and Time Delay
7	**Peripherals and Interfacing Experiments** 　i)　Traffic light control
	CYCLE-II
8	i)　Digital clock 　ii)　Key board and Display
9	i)　Printer status 　ii)　Serial interface and Parallel interface
10	A/D and D/A interface
11	Waveform Generation
12	**8051 Experiments using kits and MASM** Basic arithmetic and Logical operations
13	i)　Square and Cube program 　ii)　Find 2's complement of a number 　iii)　2's complement addition
14	Unpacked BCD to ASCII
15	Model Exam
Content beyond syllabus (if any): Stepper motor control	

* Session Duration: 150 minutes

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	Prepared by	Approved by
Signature		
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Designation	Professor/INT Associate Professor/INT	HOD/INT
Date	21.1.25	21.1.25
Remarks* :		
Remarks* :		