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|  | **Dr. P. PATTUNNARAJAM,**  **Associate Professor**  **Department of Electronics and Communication Engineering**  **Sri Venkateswara College of Engineering**  **Pennalur, Sriperumbudur – 602117**  **Mobile:9791044817**    https://www.svce.ac.in/profile-info/?id=19953 |

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| **Degree** | **Specialization** | **University** | **Year of Completion** |
| B.E. | Electronics and Communication Engineering | University of Madras | 1997 |
| M.Tech | VLSI Design | Bharath University | 2006 |
| Ph.D. | Testing of VLSI Design | Anna University, Chennai | 2020 |

**Teaching Experience: 23 Years**

**Research**

Recognized as a Research Supervisor by Anna University for guiding Ph.D. scholars (**Supervisor No- 4490012**)

**Research Areas**

* Analog VLSI
* Digital VLSI Testing
* Testing of VLSI Design
* VLSI Design
* Nano Electronics