



SVC

Sri Venkateswara
College of
Engineering

RAIC

SRI VENKATESWARA COLLEGE OF ENGINEERING

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

In Association with

ELECTRONICS AND COMMUNICATION ENGINEERS ASSOCIATION

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INSTITUTION OF ELECTRONICS AND TELECOMMUNICATION ENGINEERS-SF

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ROBOTICS & ARTIFICIAL INTELLIGENCE CLUB

28.02.2024

Proposal for conducting Guest Lecture on VLSI and System Verilog: Recent Trends and Job Opportunities

The Electronics and Communication Engineers, IETE Student Forum and RAIC cordially plan to conduct a guest lecture titled “**VLSI and System Verilog: Recent Trends and Job Opportunities**” at our college premises on **29th February 2024**. The target audience for this guest lecture includes **3rd-year students** of the ECE Department. This lecture provides valuable insights to the students into the scope of the field of VLSI.

We are honoured to have Mr.Venkatesh Rajakutti, Design Engineer, Tessolve Semiconductors Pvt Ltd., Bengaluru, as the resource person for this guest lecture. This lecture aligns with our department’s commitment to offering comprehensive learning opportunities for our students and promoting partnerships with reputed companies like Tessolve.

Venue: Function Hall.

Timing: 1 pm to 3 pm.

We kindly request your approval for the successful execution of this event.

Thank You

Dr. T. J. Jeyaprabha & Mr. S. Elangovan
ECEA, IETE-SF, RAIC Cordinators

Dr. G. A. Sathish Kumar
HOD - ECE