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SRI VENKATESWARA COLLEGE OF ENGINEERING

Department of Electronics and Communication Engineering 30/05/2019

Submitted to the Principal for Approval

As a part of faculty development, it is proposed to conduct a Faculty Development Programme in Soft Computing which is offered as an elective subject for VIII Sem ECE, CSE and IT students under Anna University, Regulations 2017.

Soft computing is an important branch of computational intelligence, where fuzzy logic, probability theory, neural networks, and genetic algorithms are synergistically used to mimic the reasoning and decision making of a human. Hence the knowledge on this subject would help the faculty members to inculcate their students, the various soft computing techniques for solving complex problems.

It is planned to conduct the FDP for 6 working days at our college from 10/06/2019 to 15/06/2019. A minimum of 18 sessions (including lab sessions) covering the entire syllabus are scheduled to be conducted. It is planned to have 8 external and 10 internal resource persons for handling the technical sessions.

The expected number of participants is 25 (approx.) - 15 external & 10 internal. The targeted audience would be our faculty and the faculty members from various Engineering colleges. A budget of Rs 41700/- is estimated and the break up details are given below.

S.No.	Details	Amount in INR
1.	Honorarium & Transport for the External experts [8 sessions]	28000/-
2.	Banner printing	700/-
3.	Brochure, certificate printing and postal charges	3000/- 2000
4.	Biscuits and Tea (For 5 days) snacks	3000/- 2000
5.	Lunch for delegates and participants	7000/-
	Total	41700/-

In this regard, I request you to kindly permit us to conduct the programme. A registration amount of Rs 750/- could be collected from the participants and hence it is requested to grant us an amount of Rs.25000/- from the college towards the conduct of FDP.

It will be helpful to the department while taking up NBA and NAAC Inspection in the forthcoming years. Hence I request you to accept this proposal in the interest of Faculty Development at our college and grant us permission and fund for the same.

Dr.S.Muthukumar
 HoD-EC

To: HoD-EC

Priority
 pl organize programs for our students/staff and not for other faculty.
 You may opt to do it as a Workshop (as it covers lab sessions, 60) and also cover students as audience with a minimal fee.

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2000
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HERE

Faculty Development Programme

On

“Soft Computing Techniques and their applications”

10-14 June 2019

REGISTRATION FORM

Name :
Designation :
Department :
Address :

Email-ID :

Mobile No :

Place:

Date:

Signature of the Applicant

ORGANIZING COMMITTEE

Convenor:

Dr.S.Muthukumar,
Head of the Department-ECE,SVCE

Coordinators:

Ms.D.Menaka ,Assistant Professor,
Department of ECE, SVCE
Ms.K.Srividhya ,Assistant Professor,
Department of ECE, SVCE
Ms.K.S.Subhashini,Assistant Professor,
Department of ECE, SVCE
Ms.S.Kalyani,Assistant Professor,
Department of ECE, SVCE
Ms.L.Anju,Assistant Professor,
Department of ECE, SVCE
Mr.M.K.Varadarajan ,Assistant Professor,
Department of ECE, SVCE
Mr.N Sathish ,Assistant Professor,
Department of ECE, SVCE

CONTACT ADDRESS

For any queries and to send completed registration forms:

Ms.D.Menaka Ms.K.Srividhya, Ms.L.Anju

Coordinator(s), Dept of ECE,
Sri Venkateswara College of Engineering
(Autonomous)

Post Bag No 1,Pennalur,
Sriperumbudur – 602117
Tamil Nadu

Phone: 91 -44 - 27152000

Extn: 225,227,229

Mobile:+91,9655955257,9444438641,9840159602

Email ID: menaka@svce.ac.in

anju@svce.ac.in,

srivi@svce.ac.in,

ksubha@svce.ac.in

kalyani@svce.ac.in

rajan@svce.ac.in

sathishn@svce.ac.in

Faculty Development Programme

On

“Soft Computing Techniques and their applications”

10-14 June 2019



Organized by

Department of
Electronics & Communication Engineering

Sri Venkateswara College of Engineering
(Autonomous)
Sriperumbudur - 602 117

www.svce.ac.in

In association with

Institute of Electronics & Telecommunication
Engineers (IETE)

ABOUT SVCE

Sri Venkateswara College of Engineering (SVCE), a premier self financing institute started in the year 1985 and became autonomous institution in the year 2016. The College conducts 10 BE / B.Tech Degree courses and 10 PG programs in Engineering. 11 research centers approved by Anna University cater to MS (by Research) and Ph.D programs. The courses are approved by AICTE and affiliated to Anna University, Chennai. The college is also accredited by National Assessment and Accreditation Council (NAAC). Many programs are accredited by National Board of Accreditation (NBA). The college is also certified by IRQS for ISO 9001-2015. The college is spread over 95 acres of land, situated at about 37 kms from Chennai on the Chennai – Bangalore National Highway (NH-4) at Pennalur, Sriperumbudur. A 280Mbps leased Internet connectivity is available. The campus utilizes the Intranet facilities for all internal communication and databases. The campus is Wi-Fi enabled. The main objective of this institution is to impart knowledge to faculty members, undergraduate postgraduate and research students.

ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering was started in the year 1985 with an under graduate program in Electronics and Communication Engineering. A PG program in Communication Systems was started in the year 2002 and PG program in Applied Electronics was started in the year 2012. The department has strength of around 750 students guided by a team of devoted and diligent faculty members. There are about 33 faculty members in the department, 9 of them being doctorates. The Department organizes a National Level Student Technical Symposium "UPAGRAHA", a National Level Conference "National Conference on Signal Processing, Communication & Networking (NCSCN)" every year and also organizes Workshops, Seminars and FDPs periodically.

THE FDP

With the emergence of novel Data processing strategies, optimisation has been a significant area of study and research. Most of the soft computing techniques find wide area of application in the field of Information security, remote sensing, video processing, multimedia communication and medicine.

In order to keep pace with the rapid advances in optimizing the data set, vision, language, artificial intelligence, swarm and genetic algorithms, we would like to organize a faculty development program that would facilitate exchange of ideas between the researchers and the experts in all these areas. This would enable a discussion on various aspects of optimization strategies that pertain to scene understanding and description.

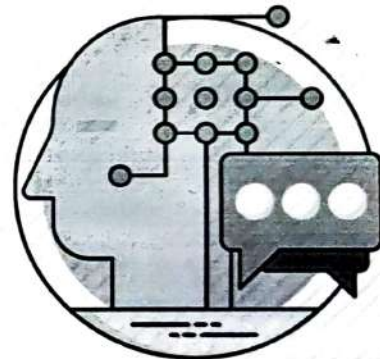


WHO CAN PARTICIPATE

The workshop is suitable for all faculty members, researchers and people from R & D areas in industries of the ECE/CSE/IT/EEE domain. All people who are aspiring to learn and teach subject relevant to soft computing are also welcome to attend the program.

TOPICS TO BE COVERED

- ✓ Soft computing techniques-An introduction
- ✓ Advances in Artificial Neural Networks, Types-BPN,BAM,ART
- ✓ Optimisation strategies and Algorithms
- ✓ Fuzzy logic
- ✓ Genetic algorithms
- ✓ Hybrid soft computing techniques and its applications
- ✓ Hands-on sessions



IMPORTANT DATES

Last date for registration : 06.06.2019
Last date for intimation : 07.06.2019
(through Email only)

COURSE FEE DETAILS

Faculty, Researchers, Industrial Participants :Rs. 500/-

SCHEDULE FOR THE FACULTY DEVELOPMENT TRAINING PROGRAMME

Title: Soft Computing Techniques and their applications

Branch: Electronics and Communication Engineering

Morning Tea Break: 10-00 AM -10-15 AM

Lunch: 12.15 AM 1.15 PM

Afternoon Tea Break: 03.00 PM-03.15 PM

Sl. No	Date / Day	9.00 AM to 10.00 AM	10.15 AM to 12.15 PM	01.15 PM to 03.00 PM
1	10.06.2019 Monday	Registration & Inauguration-Introduction to soft computing	Applications of Soft computing using MATLAB Mr.Rajkumar Design Tech	Deep Neural network and its applications using MATLAB Mr.Rajkumar Design Tech
2	11.06.2019 Tuesday	Introduction to Artificial Neural Networks and Fuzzy logic Dr.R.Gayathri,Asso.Prof,ECE SVCE	Supervised Learning Networks Dr.Chandrabose Aravindhan Professor,CSE SSN College of Engineering	Applications of Supervised Learning Networks,Associative Memory networks Live Demo Dr.N.Rajeswari,Asso.Prof,CSE SVCE
3	12.06.2019 Wednesday	Introduction to Genetic Algorithm,Simple GA modelling,GA operators Dr.R.Jayabhaduri,Asso.Prof,CSE SVCE	GA Convergence,Multilevel optimisation,Advances in GA Dr.R.Jayabhaduri,Asso.Prof,CSE SVCE	Introduction to Fuzzy logic Fuzzy rules, Fuzzy reasoning, Fuzzy inference systems,Overview of Fuzzy expert systems Dr.K.S.Gayathri,Asso.Prof,CSE SVCE
4	13.06.2019 Thursday	Fuzzy Logic-Membership functions, Fuzzy relations, Fuzzification and defuzzification methods Ms.D.Menaka/Ms.K.Srividhya/Ms.L.Anju/ Ms.S.Kalyani Asst.Prof,ECE SVCE	Practical applications of Machine learning Dr.S.Sridhar,Asso.Prof Dept of Information Science and Technology,Anna University	Adaptive Neuro fuzzy inference systems,GA in Fuzzy logic controller design,Fuzzy logic controlled GA based optimisation Mr.S.P.Sivagnana Subramanian,Asst Prof SVCE
5	14.06.2019 Friday	Overview of Hybrid soft computing systems,Neuro fuzzy and Neuro Genetic hybrids Ms.T.J.Jeyaprabha,Asst.Prof SVCE	Unsupervised Learning Networks, Kohenen self organising feature map, Learning Vector Quantisation, ART I Network Architecture. Dr. D. Thenmozhi, Asst.Prof.CSE SSN College of Engineering	Applications of Hybrid Soft computing Techniques-Multispectral Image fusion,Travelling salesman problem,Fuzzy logic based air conditioner controller Dr.L.Vanitha,Asso.Professor Prathyusha Engineering College

K. S. Thiya

Handwritten signature
H. K. E

Prof. Dr. Chandrabose Aravindan

Email: aravindanc@ssn.edu.in

Chandrabose Aravindan is a Professor of Computer Science and Assistant Director (School of Advanced Software Engineering) at SSN College of Engineering, Chennai, India, since July 2002. He received his B.E. degree with honors in Computer Science and Engineering from National Institute of Technology, Trichy, India, in 1986 and M.E. and Ph.D. degrees in Computer Science from Asian Institute of Technology, Bangkok, Thailand, in 1990 and 1995, respectively. His post-doctoral research was with the University of Koblenz, Germany, during 1995-1997, where he worked as a Research Scientist on a funded project in the area of disjunctive logic programming. He has been in academia as a professor of Computer Science since 1998.

Aravindan is a member of the Machine Learning Research Group of SSN and his research interests include machine learning and pattern recognition, logic programming and formal methods, and high-performance computing. Twelve research scholars have already completed their PhD under his supervision and he is currently guiding one more scholar. Aravindan has completed five funded research projects, and has published about 50 papers in international journals and conferences.

Aravindan is a member of several professional bodies such as ACM, IEEE, IEEE Computer Society, Computer Society of India (CSI), and Indian Society for Technical Education (ISTE).



(L. Anju)

HOD-EC
(Dr. S. Meethukumar)

ATTENDANCE

S.No	Name of the Participant	Designation	Institution	Signature										
				FN	AN	FN	AN	FN	AN	FN	AN	FN	AN	
				10-06-2019	11-06-2019	12-06-2019	13-06-2019	14-06-2019						
1	A. Bharathipriya	AP	SVCE	B	B	B	B	B	B	B	B	B	B	B
2	M.K. Varadarajan	AP	SVCE	V	V	V	V	V	V	V	V	V	V	V
3	V. Ranjith	AP	SVCE	R	R	R	R	R	R	R	R	R	R	R
4	S. Jerusha	AP	SVCE	J	J	J	J	J	J	J	J	J	J	J
5	D. Amudhavalli	AP	SVCE	D	D	D	D	D	D	D	D	D	D	D
6	S. Senthilrajan	AP	SVCE	S	S	S	S	S	S	S	S	S	S	S
7	N. Sathish	AP	SVCE	N	N	N	N	N	N	N	N	N	N	N
8	S.P. Sivagnana Subramanian	AP	SVCE	S	S	S	S	S	S	S	S	S	S	S
9	P. Anil	AP	SVCE	P	P	P	P	P	P	P	P	P	P	P
10	S.V. Muthunagai	AP	SVCE	M	M	M	M	M	M	M	M	M	M	M
11	R. Tyswarya	AP	SVCE	T	T	T	T	T	T	T	T	T	T	T
12	B. Bhuvaneshwari	AP	SEC	B	B	B	B	B	B	B	B	B	B	B
13	V. Karthiga	AP	SEC	V	V	V	V	V	V	V	V	V	V	V
14	S. Senthil Kumar	AP	GRT Coll. of E&T	S	S	S	S	S	S	S	S	S	S	S
15	Rajaram.V	AP	SVCE	R	R	R	R	R	R	R	R	R	R	R
16	K. Manikandan	AP	AMET University	M	M	M	M	M	M	M	M	M	M	M
17	T. Kosala	AP	UCE Kanchipuram	T	T	T	T	T	T	T	T	T	T	T
18	V. Jayawandhar	ASP	RMK CET	V	V	V	V	V	V	V	V	V	V	V
19														
20														
21														
22														

K. S. -thD. Subramanian
HOD/ECE

SRI VENKATESWARA COLLEGE OF ENGINEERING

Department of Electronics and Communication Engineering

Faculty Development Programme
on
Soft Computing and their Applications

10th-14th June, 2019

A Report

The Department of Electronics and Communication Engineering organized a Faculty Development Training Programme on Soft Computing and their Applications from 10th-14th June, 2019. The Programme was inaugurated by Dr.R.Anitha, Professor and HOD/CS. She delivered an introductory talk on the significance of Soft Computing and their Applications with various real time scenarios where optimisation plays a vital role.

A total of 14 lecture sessions and 2 hands on sessions covering all the major topics that included Artificial Neural Networks, Genetic Algorithms, Fuzzy Logic and Hybrid Soft Computing Systems. These technical lectures and hands on sessions were handled by the experts invited from various reputed institutions, industry and also senior faculty members of the host college.

This FDP is devoted to fundamental theory, recent developments and research outcomes addressing the related theoretical and practical aspects of various Soft Computing techniques. The FDP was conducted successfully with 18 participants from various academic institutions.

The programme provided an insight to the recent updates and current technologies practised in the industry, which was well handled by the industrial expert. The participants provided a positive feedback about the sessions handled by both the academic and industrial expert. The practical sessions that demonstrated the use of MATLAB and Python were also helpful to the participants in understanding the applications of Soft Computing.

The Valedictory function was presided over by Dr.Sudhakar K Bharathan, Professor, Department of EEE, SVCE



We would like to express our gratitude to our Management and the Principal for the approval and support including financial assistance for the conduct of the programme. We also express our heartfelt thanks to IETE SVCE Chapter for the financial assistance towards the conduct of the programme.


HOD - ECE, SVCE

K. S. Raju
co-ordinator

SRI VENKATESWARA COLLEGE OF ENGINEERING
DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

Faculty Development Program
on
Soft Computing Techniques and its applications

10/6/2019 to 14/6/19

Feedback from Participants

Name : S.V. Muthunagai
Designation : AP/CS
College : SVCE
Contact Address : Sriperumbudur

Email ID : muthunagai@svce.ac.in
Mobile No : 9789623092
Organisation of the program : Very Good.

Comments : The speakers are really good. All the speakers gave deep lecture in their topic. All the concepts taught in FDP was understood clearly.

Suggestion for Improvement : -


Signature

SRI VENKATESWARA COLLEGE OF ENGINEERING
DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

Faculty Development Program
on
Soft Computing Techniques and its applications

10/6/2019 to 14/6/19

Feedback from Participants

Name : V. Karthiga
Designation : AP
College : Saveetha Engineering College
Contact Address : Saveetha Engineering College, Thandalam.

Email ID : Karthiga@saveetha.ac.in

Mobile No : 9791113869

Organisation of the program : Program was well organised. Everything was on time.

Comments : The overall session was very informative and new. The concepts of the topics covered was clear and understandable.

Suggestion for Improvement : More Hands on session and Applications

V. Karthiga
Signature

Sri Venkateswara College of Engineering

Sriperumbudur-602117



Certificate of Participation



This is to certify that Mr/Ms _____
of _____ has attended
Faculty Development Programme on "SOFT COMPUTING TECHNIQUES AND
THEIR APPLICATIONS" during 10th - 14th June 2019.

organized by
Department of Electronics and Communication Engineering

Co-ordinator

Convenor