A Report on Two Days National Workshop on "Antenna 3D Simulation TechniquesUsing CST Studio Suite" Conducted By Department of ECE, Sri Venkateswara College of Engineering.

The Two Days National Workshop on "Antenna 3D Simulation Techniques Using CST Studio Suite" was inaugurated in the presence of Mr.Chandresh Dhote, Application Engineer, M/s.Jyoti Electronics, Ahmedabad, on 27th March 2019 and Welcome address was given by Dr.S.MUTHUKUMAR, Professor & Head-Department of ECE followed by the Inaugural address by Dr. H.Umma Habiba. Professor / ECE Department.

Mr.Chandresh Dhote talked about the below mentioned topics for the knowledge enhancement of our participants with the detailed Live Demo by highlighting the various features of CST Studio Suite that are available for 3D EM Simulation with specific reference to both Antenna & Filter design.

A presentation with following outline was discussed

- 1. Introduction to CST Studio Suite with its new features.
- 2. Designing of Microstrip Patch Antenna Involving Inset Cut Feed and Co-axial Feed
- 3. Array Antenna Design
- 4. Design Methodology of MIMO and Dipole Antenna
- 5. Design of Reflector antenna with Horn and far field feeding
- 6. Designing of SIW filter & Other RF components
- 7. Discussion on Inbuilt examples-SAR calculation and Co-site interference of mobile phone
- 8. Challenges involved in the design of HF components

We have received unconditional support from our resource persons during the event and particularly the efforts taken by them in explaining various design concepts with the required simulations in line with our requirements were remarkable. All our participants got benefited by attending this informative workshop.

Faculty Members, Research Scholars and PG students from other institutions participated with lots of interest and around 30 participants benefited from this Two Days National Workshop. The participant obtained the required clarifications for their doubts by having an interactive sessions with your application engineer and enjoyed thoroughly all the sessions. Participants also enriched their understanding about the EM simulation software and learnt the real time implementation of various design and modelling principles.

Later during the afternoon session of 28th March 2019, the valedictory session was arranged and the same was presided by Dr.G.Padmavathy, Professor / ECE Department. After obtaining the feedback from our participants, the event was concluded with Certificate Distribution followed by Vote of Thanks by Mr.S.Senthil Rajan, AP/EC.

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Workshop Photos:



Inauguration of Workshop in the presence of Mr.Chandresh Dhote, Application Engineer, M/s.Jyoti Electronics, Ahmedabad



Welcome address by Dr.S.MUTHUKUMAR, Professor & Head-Department of ECE

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The Detailed Live Demo Given By Mr.Chandresh Dhote, Application Engineer, M/s.Jyoti Electronics, Ahmedabad

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The valedictory session was presided by Dr.G.Padmavathy, Professor / ECE Department



Vote of Thanks by Mr.S.Senthil Rajan, AP/EC

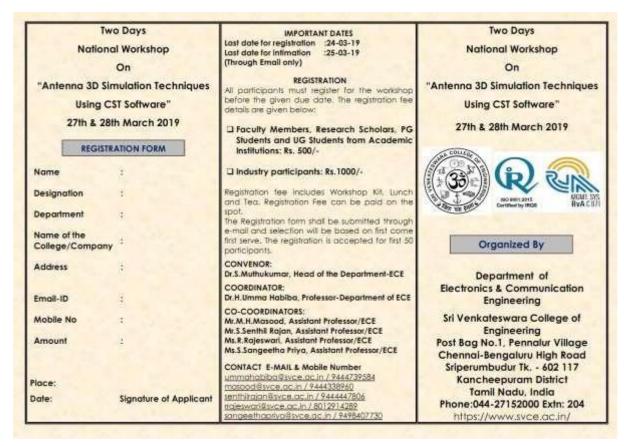


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Workshop Brochure:





Engineering (SVCE), a premier self-financing engineering college, was started in the year 1985 and is managed by Sri Venkateswara Educational and Health Trust. The college conducts 10 B.E / B.Tech Degree courses and 10 PG Courses in Engineering and Technology.

The college is autonomous and affiliated to Anna University, Chennal and is accredited by National Assessment and Accreditation Council (NAAC) and also by National Board of Accreditation (NBA),

THE DEPARTMENT

The Department of Electronics and Communication Engineering was established in the year 1985 with an Under Graduate Programme in Electronics & Communication Engineering.

It has strength of around 800 students guided by a team of devoted and dilgent faculty members.

The department is well equipped with state of the art laboratory facilities and software like Tanner Tools, LabView, ADS, IE3D, latest versions of MATLAB and Math CAD etc.,

Every year, the Department regularly organizes a National Level Student Technical Symposium "UPAGRAHA", a National Level Conference "National Conference on Signal Processing, Communication & Networking (NCSCN)" and also organizes several Workshops, Seminars and EDPs etc..

The Department has also secured AICTE funded R & D projects. The Department has signed MoUs with reputed industries,

ABOUT THE WORKSHOP

The purpose of this workshop is to provide hands on training on design of Antenna & HF Components using CST Microwave Studio.

MWS) is a specialist tool for the 3D EM simulation of high frequency components. CST MWS enables the tast and accurate analysis of high frequency (HF) devices such as antennas, filters, couplers, planar and multi-layer structures for ensuring both Signal Integrity (SI) and EMC Compatibility,

CST MICROWAVE STUDIO® (CST

TOPICS TO BE COVERED

- > Overview of CST Software
- Design Example of Basic Microstrip Patch Antenna & Antenna Array
- Design & Simulation of Horn Antenna and Reflect Array Antenna
- > Design & Simulation of MIMO Antenna
- > Design & Simulation of Multilayer Filter
- Challenges Involved in the Design of Microwave Components / Circuits

RESOURCE PERSONS:

Experts from Jyoti Electronics,

- Ahemadabad, Gujarat
- (Authorized Representative for CST in India)

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Workshop Invitation:



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Sri Venkateswara College of Engineering Post Bag No.1, Pennalur Village, Chennai - Bengaluru High Road, Sriperumbudur Tk. 602117 Kancheepuram District, TAMIL NADU, INDIA



Department of Electronics & Communication Engineering

Two Days National Workshop on "Antenna 3D Simulation Techniques Using CST Software" 27^{th&} 28th March 2019

Technical Program Schedule

	MARCH 27, 2019 (Wednesday)
8.30-9.30	Registration
9.30-9.40	Welcome Address – Dr.S.MUTHUKUMAR, HOD-Department of ECE
9.40-9.50	Inaugural speech Dr. H. UMMA HABIBA, Professor, Department of ECE, Sri Venkateswara College of Engineering (SVCE)
9.50–10.40	Live Demo by Mr.CHANDRESH DHOTE Application Engineer Jyoti Electronics, Ahmedabad Title: Introduction of CST with new features
10.40-10.50	Tea Break
10.50-11.30	Live Demo on Designing of Microstrip Patch Antenna using inset cut feed and coaxial feed
11.30-12.45	Results Analysis
12.45-13.30	Lunch Break
13.30–14.10	Live Demo on Array Antenna Designing
14.10–14.15	Tea Break
14.15-15.15	Live Demo on MIMO and Dipole antenna designing

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Two Days National Workshop on "Antenna 3D Simulation Techniques Using CST Software" 27^{th&} 28th March 2019

Technical Program Schedule

	MARCH 28, 2019 (Thursday)
9.00-10.30	Live demo on Reflector antenna with Horn and far field feeding
10.30-10.45	Tea Break
10.45-12.45	Live Demo on SIW filter & RF components
12.45-13.30	Lunch Break
13.30-14.00	Discussion on Inbuilt examples-SAR calculation and Co-site interference of mobile phone
14.00-14.10	Tea Break
14.10-14.30	Valedictory Function & Certificate Distribution Dr.G.PADMAVATHY, Professor Department of ECE, Sri Venkateswara College of Engineering (SVCE)
14.30-15.00	Feedback & Vote of Thanks Mr.S.Senthil Rajan, Assistant Professor, Department of ECE, Sri Venkateswara College of Engineering (SVCE)

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Department of Electronics & Communication Engineering

Two Days National Workshop on "Antenna 3D Simulation Techniques Using CST

Software"

27th & 28th March 2019

Attendance Sheet

Attendance energy									
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