DECLARATION

The above information is true to the best of my knowledge. I agree to abide by the rules and regulations governing the course. If selected, I shall attend the course for the entire duration. I also undertake the responsibility to inform the coordinators in case I am unable to attend the course.

Place:

Date: Signature of the Applicant

SPONSORSHIP CERTIFICATE

Mr / Ms / Dr_____

_____ is an employee / student of our institute and is hereby sponsored. He / She will be permitted to attend the course for the entire duration, If selected.

Office Seal   Signature of the
                                    Sponsoring authority

Place:

Date:

Application form completed in all aspects is to be sent to:

Dr. G.A.SATHISH KUMAR
Professor
Co-ordinator

Dept of Electronics and Communication Engg,
SRI VENKATESWARA COLLEGE OF ENGINEERING
Post Bag No.1, Pennalur Village
Chennai - Bengaluru High Road
Sriperumbudur Tk. - 602 117
Kancheepuram District
TAMIL NADU.

RESOURCE PERSONS

Sessions will be handled by experts from Pantech ProLabs, Chennai.

ELIGIBILITY

Personnel from AICTE approved Engineering Colleges (PG Students), Industries and Research Scholars.

REGISTRATION

Registration fee: **Rs.400**

HOW TO APPLY

The applicants should send their applications in the specified format along with a DD drawn in favor of **"The Principal, Sri Venkateswara College of Engineering",** payable at Indian Bank, Sriperumbudur should reach on or before **28.01.2017.** If selected, they should confirm their participation as per schedule below.

SCHEDULED DATES

Last Date for Receipt of Applications   : 28.01.2017

Intimation of Selection by e-mail   :29.01.2017

TRANSPORT

The participants can avail the SVCE bus facility to attend this workshop, For transportation details, please refer to our college website.

SRI VENKATESWARA COLLEGE OF ENGINEERING

(Autonomous - Affiliated to Anna University)

(An ISO 9001:2008 Certified College)

PRESENTS

"TWO DAYS WORKSHOP ON FPGA based system Design for image Processing algorithms"

1st and 2nd February, 2017

CONVENOR
Dr.S.MUTHUKUMAR, HOD/EC

CO-ORDINATORS

Dr.G.A.SATHISH KUMAR, Professor

Mr.M.ATHAPPAN, Assistant Professor

Mr.S.SARAVANAN, Assistant Professor

Organized by

Dept. of Electronics & Communication Engg.,
SRI VENKATESWARA COLLEGE OF ENGINEERING

Mobile: 9445265296,9445205156,9884806830

Tel: 044-27152000, Ext: 201, 203.

E-mail: sathish@svce.ac.in
         athappan@svce.ac.in
         saravanans@svce.ac.in
Website: www.svce.ac.in

THE PROGRAMME CONTENTS OF THE WORKSHOP ARE AS FOLLOWS:

Introduction to fundamentals of FPGA/CPLD, Xilinx FPGA/CPLD architecture functional block evaluate. Introduction to Xilinx EDK Tool- Processor based design implementation and coding technique.

Demonstration of microprocessor based design – writing C code to control FPGA interface with running sample codes. Training using Spartan -3 FPGA Image Processing Kit- new project creating using Xilinx Platform Studio and running C code in FPGA.

Introduction of Image Processing using FPGA, Soft-core processor design for image processing, configuring IP cores for peripheral interface. Image processing algorithm implementation- Edge detection, Median Filtering, Discrete Wavelet transform.

Hands on implementation of Image processing algorithms, and output in PC through VB GUI.

ABOUT THE INSTITUTION

Sri Venkateswara College of Engineering (SVCE), one of the Premier Technical Institutions in Tamilnadu, was established in 1985, approved by AICTE New Delhi and affliated to Anna University, chennai has completed 30 years of dedicated and excellent service in the field of technical education. The College is situated on the Chennai – Bangalore National Highway (NH4) about 37 km south-west of Chennai. The college offers 10 UG programmes and 10 PG programmes. The National Board of Accreditation has accredited many of the eligible programmes, and SVCE is an ISO 9001:2008 certified institution.

ABOUT THE DEPARTMENT

The Electronics and Communication Engineering department occupies a prominent place in the chronicles of its academic history. The specialization of the faculty includes Telecommunication engineering, Computer Networks, Control Systems, VLSI, Digital Signal Processing, Microwave Systems, Applied Electronics, Microprocessor and Micro Controllers etc. The department has excellent laboratory facilities in the areas like Networking, Microprocessors & Microcontrollers, Electronic Devices, Communication, RF, Microwave, VLSI, Digital Signal Processing, Applied Electronics, with latest software. The department offers four year UG programme in ECE and two year PG programmes in Communication Systems and Applied Electronics. The department is a approved research center by Anna University. Around 40 Research scholars are pursuing their full time and part time Ph.D in the research center.

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Kancheepuram District, TAMIL NADU.
PRESENTS

"TWO DAYS WORKSHOP ON FPGA based system Design for image Processing algorithms"

1st and 2nd February, 2017

APPLICATION FORM

Name:

Department:

Educational Qualification:

Age:

Organization:

Designation:

Address for Communication:

E-mail ID:

Mobile Number:

Signature of the   Signature of the
Sponsoring Authority   Applicant

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PRESENTS

"TWO DAYS WORKSHOP ON FPGA based system
Design for image Processing algorithms"

1st and 2nd February, 2017

APPLICATION FORM

Name: K SWETHA

Department: M.E APPLIED ELECTRONICS

Educational
Qualification: B.E

Age: 22

Organization: —

Designation: —

Address for
Communication: SVCE

E-mail ID: swekannadasan @ gmail.com

Mobile Number: 7708284952

Signature of the
Sponsoring Authority

Signature of the
Applicant

A Report on "FPGA Based System Design for Image Processing Algorithms" Workshop Organized during 01st and 02nd February 2017

The Two Day 'FPGA Based System Design for Image Processing Algorithms' was held on February 01st and 02nd, 2017 conducted by Electronics and Communication Engineering Department, SVCE in association with Pantech Prolabs, Chennai. The workshop started with the prayer song by Ms.Kaviyashri.
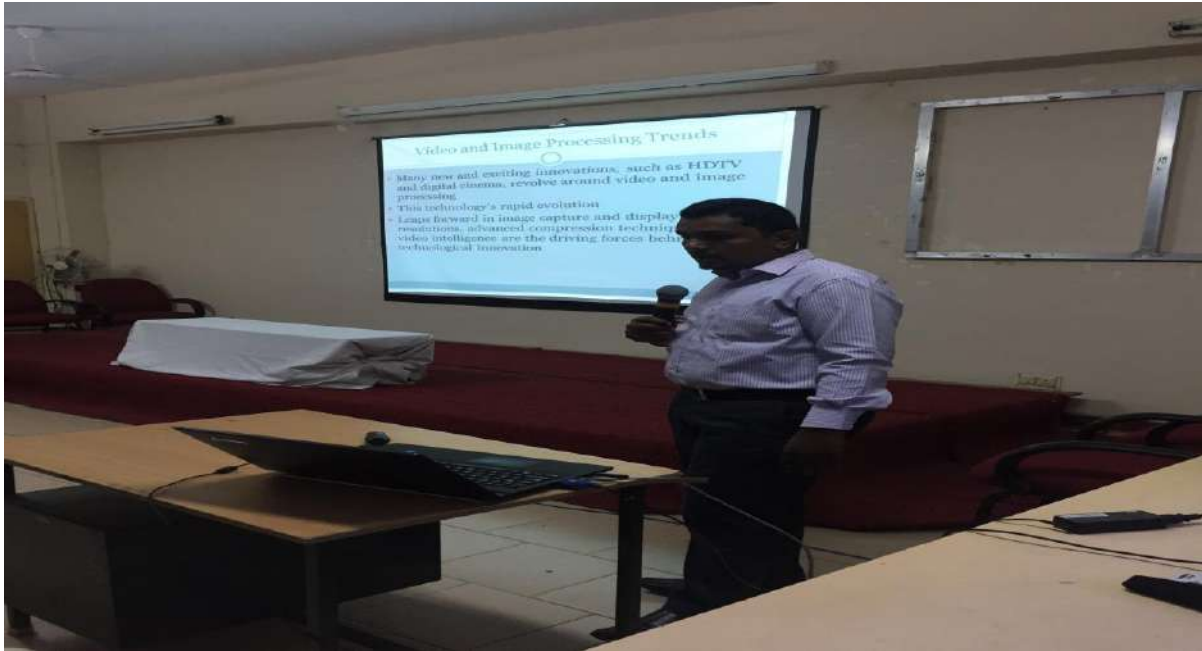

The Welcome address was given by Dr.S.Muthukumar, Head of the Department, ECE. The introduction to Pantech Prolabs was shared by Dr.G.A.Sathish Kumar, Professor, ECE.

Day1:

The Key Note address on 'Fundamentals on FPGA/CPLD' was given by Mr.Shahul Hameed Akthar. The speaker also introduced about Xilinx EDK tool.

In the afternoon session, hands-on training using Virtex board for image processing algorithm was given. The speaker also demonstrated about how to create a new project using Xilinx platform.

Day2:

The Key Note address was given by Mr.Shahul Hameed Akthar. In the first session, the speaker gave an introduction about soft core processor design for image processing applications. The speaker also briefed about configuring IP cores for peripheral interface.

The second session involved hands-on training on the image processing algorithm implementation such as edge detection, median filtering and discrete wavelet transform.

The valedictory address was given by Dr.S.Muthukumar, Head of the Department (ECE), the momento was given to the Speaker Mr.Shahul Hameed Akthar, Pantech Prolabs, Chennai. The certificates were also distributed by HOD/ECE to the participants. The vote of thanks was given by Mr. Mohit Jhunjhunwala, Student (ECE).

Sri Venkateswara College of Engineering

Sriperumbudur-602117

Certificate of Participation

This is to certify that Mr/Ms_____

of_____has attended

"TWO DAYS WORKSHOP ON FPGA BASED SYSTEM DESIGN FOR IMAGE PROCESSING ALGORITHMS"
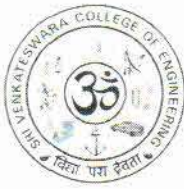
held on 1st and 2nd February, 2017

Co-ordinator   **Convenor**

ORGANISED BY

Department of Electronics and Communication Engineering

SRI VENKATESWARA COLLEGE OF ENGINEERING

FEEDBACK FROM THE SPEAKER - GUEST LECTURE

DEPARTMENT : ECE

Name of the Speaker	: Paul Kannan - G
Designation	: R &D Engineer Pantech Prolabs
Institution/University/Organisation	: Pantech Prolabs
Mobile / E-mail	: 9500763347 / PaulKannan@pantechsolutions.net
Title of the Lecture	: FPGA Based Image Processing.
Date	: 1/2/2017 - 2/2/2017
Time	: 9.00 AM to 3.30 PM.
Venue	:
Comments by the Speaker	: Good Support from Students and Staff.
Suggestions for improvement	:
Signature of the Speaker	:

Signature of Coordinator

Signature of HOD

SRI VENKATESWARA COLLEGE OF ENGINEERING
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TWO DAYS WORKSHOP ON FPGA BASED SYSTEM DESIGN FOR IMAGE
PROCESSING ALGORITHMS ON 1ST AND 2ND FEBRUARY 2017

S.NO	NAME	DESIGNATION	INSTITUTION
1	ARAVINDHAN K	STUDENT	SVCE
2	DEEPA T	STUDENT	SVCE
3	DEEPIKA G	STUDENT	SVCE
4	FINCY MOL F	STUDENT	SVCE
5	JOSE NISHA J	STUDENT	SVCE
6	KOPPERUNDEVI P	STUDENT	SVCE
7	MAGESHWARI N	STUDENT	SVCE
8	NADAR JAMES FRANCIS JOSEPH ROY	STUDENT	SVCE
9	PAVITHRA G	STUDENT	SVCE
10	RAJALAKSHMI P	STUDENT	SVCE
11	SWETHA K	STUDENT	SVCE
12	YUVARAJ E	STUDENT	SVCE
13	AKILA A	STUDENT	SVCE
14	BHARATHI D	STUDENT	SVCE
15	DEEPIKA S	STUDENT	SVCE
16	HEMAVATHI K	STUDENT	SVCE
17	JOTHILAKSHMI K	STUDENT	SVCE
18	KALAIVANI P	STUDENT	SVCE
19	LAKSHMI THARA R	STUDENT	SVCE
20	MOHAN C	STUDENT	SVCE

SRI VENKATESWARA COLLEGE OF ENGINEERING
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
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PROCESSING ALGORITHMS ON 1ST AND 2ND FEBRUARY 2017

21	MOHANA SUNDARAM R	STUDENT	SVCE
22	NIVEDHA P	STUDENT	SVCE
23	RAMYA R	STUDENT	SVCE
24	SNEHA V	STUDENT	SVCE
25	SRIDEVI L	STUDENT	SVCE
26	VIJAYALAKSHMI S	STUDENT	SVCE
27	VINMATHY M	STUDENT	SVCE

M. Sseeean

Head of the Department
Electronics and Communication Engineering
Sri Venkateswara College of Engineering
Sriperumbudur - 602 117. India