SRI VENKATESWARA COLLEGE OF ENGINEERING

Department of Electronics and Communication Engineering

Date: 20/02/2017

Submitted to the Principal for Approval

Through HOD- EC

As a part of research activities, we propose to conduct a National Conference on "Signal Processing, Communication and Networking" NCSPCN '17 at our college during 05-04-2017 & 06-04-2017. We have identified 4 speakers from Indian Institute of technology-Madras & CEG - Anna University for the technical talks. The targeted audience would be UG, PG students, Research scholars and Faculty from various Engineering colleges. A budget of Rs 37,500/- is estimated, the break up details are given below

BUDGET for National Conference on

S.No	Details	Amount in INR
1.	Honorarium & Transport for the experts [4 sessions]	15,000/-
2.	Stationery and Certificates	2500/-
3.	Brochures and banners	2500/-
4.	Lunch and Snacks	5000/-
5.	Pre - conference printing	5000/-
6.	Publications of Proceedings	7500/-
7.	Projected Expenditure	37,500/-
8.	Registration Fee Per candidate	750/-
9.	Expected Revenue through Registration (Expected No. of Participants - 40)	30,000/- (750*40)
10.	External Sponsorship	5,000/-
11.	Amount Requested from College {Projected Expenditure -Registration Fee & External Sponsorship = 37500-35000}	2,500/-

We are expecting Rs.35,000/- through Registration and External sponsorship. It is we are expecting advance from the college for doing preliminary work and the requested to provide Rs.20,000/- in advance from the college for doing preliminary work and the advance amount will be returned back to College after receiving the registration amount.

We request you to kindly accept this proposal in the interest of research at our college and grant us the fund for the same. Kindly do the needful.

The proposal is resubmitted

The proposal is resubmitted

The proposal is resubmitted

Corrections.

Deturbury.

10. 27. 443 E. after Deturbury.

HOD-EC

A Report on NCSPCN'17 organized on April 5th and 6th 2017 by the Department of **ECE**

ing. It will also offer an opportunity to faculty and

TOPICS OF CONFERENCE

The following areas form the broader scope of the conference which shall include but not limited to the topics listed as under and can be extended based on the creativity of the authors.

- Ad hoe & Sensor Networks
- Bio-Medical Signal Processing
- Channel Modeling, Coding & Mean
- Cognitive Radio & Dynamic Spectrum Mina
- DSP Algorithms & Applications
- **←** Electronic Systems
- Microwave (Antenna/Me s/EMI/EMC)
- MIMO Systems
- Networking & Com
- Security in Commu ication & Networking
- Signal, Image Processing & Machine Lear

BOUT THE COLLEGE

earch centers approved by Anna University cater to S(by Research) and Ph.D programmes. The courses are approved by AICTE and affiliated to Anna University, Chennai, The college attained autonomous status in 2016.



The college is accredited by National Assessment and Accreditation Council (NAAC). The college is also certified by IRQS for ISO 9001-2008. The college is situated in serene environment about 37 Kms from Chennai and situated on the way of Chennai - Bangalore National Highway (NH4) at Pennalur, Sriperumbudur, in Kanchipuram district. The campus is Wi-Fi enabled.

ABOUT THE DEPARTMENT

The Department of ECE was started in the year 1988. The nate program (M.E) in Communi ion Systems was started in 2002 and Applied Electronics in 2012. There are about 44 faculty members in the department and 7 of them are doctorates. The department is well equipped with lab facilities and software tools like IE3D, ADS, Lab View, er Tools, MATLAB. The UG programme in the



Sri Venkateswara College of Engineering

(An Autonomaus Institution Affiliated to Anna University, Che

> National Cont on

SIGNAL PROCESSING,

COMMUNICATION NETWORKING

"NCSPCN-2017"

APRIL 5th & 6th 2017

CONVENOR Dr. S. Muthukumar, HOD/ECE

COORDINATORS athish Kumar, Professor/ECE

Ms. Tel. Jeyanrabha, AP/ECE Organized

by

Department of Electronics & Communication

Sri Venkateswara College of Engineering. Pennalur, Sriperumbudur-602 117.

SRI VENKATESWARA COLLEGE OF ENGINEERING

Department of Electronics and Communication Engineering

National Conference on "Signal Processing, Communication and Networking"

Session - I Paper Presentation Programme AGENDA: 6* April 20.7 - DAY 2 Session - III Paper Presentation Programme AGENDA: 6* April 20.7 - DAY 2 Session - III Paper Presentation PROGRAMME AGENDA: 6* April 20.7 - DAY 2 Session - III Session - III Paper Presentation PROGRAMME AGENDA: 6* April 20.7 - DAY 2 Session - III Session - III Session - III Paper Presentation PROGRAMME AGENDA: 6* April 20.7 - DAY 2 Session - III Session - II	FROGR	CAMME AGENDA: 5*	April 2017 - DAY I	
Inauguration of the event 9.05 - 9.15 hrs Welcome Address by Dr. S. Michaelmar, Hoddens by Dr. S. Asathish Kumar, Prof., ECE. SV. 09.15 - 09.20 hrs Session - 1 Design Methodologies for Next Generation Custom Accelerators: High-Level Synthesis for System-level Solutions. 10.20 - 10.30 hrs Session - 1 Paper Presentation 10.30 - 12.15 hrs VLSI "Image security." 1:00 - 2:00 hrs Session - II Paper Presentation 1:15 - 1:00 hrs PROGRAMME AGENDA: 6 April 20.7 - DAY 2 Session - III Underwater Vehicles. Session - IV Poper Presentation 1:15 - 1:15 hrs Valedictory session 2:45 - 3.00 hrs Valedictory session Valedictory session Valedictory session Valedictory session Veledictory session		8:30 - 9:00 hrs	Registration	
Inauguration of the event 9:05 - 9:15 hrs Dr. S.Muthukumar, Hob-ECE, SVCE NCSPCN'17 - Brief Address by Dr. G.A.Sathish Kumar, Prof., ECE. SV 09:15 - 09:20 hrs Session - I Design Methodologies for Next Generation Custom Accelerators: High-Level Soniticis for System-level Solutions" 10:20 - 10:20 hrs Session - I Paper Presentation 10:30 - 12:15 hrs 12:15 - 1:00 hrs Session - III Image security" Session - III Paper Presentation 10:00 - 2:00 hrs PROGRAMME AGENDA: 6" April 20, 7 - DAY 2 Session - III Inderwater Vehicles" 9:00 - 10:30 hrs Session - III Inderwater Vehicles" 9:00 - 10:30 hrs Session - III Inderwater Vehicles Super Presentation 10:15 - 12:15 hrs Session - IV Paper Presentation 1:15 - 2:30 hrs Valedictory session 1:45 - 3:00 hrs Valedictory session Valedictory Address and Certificate Distribution		9:00 - 9:05 hrs	Prayer Song	
Session - I Design Methodologies for Next Generation Custom Accelerators: High-Level Synthesis for System-level Solutions* 10.20 - 10.30 hrs 10.20 - 10.30 hrs 12:15 - 1:00 hrs Session - II I'mage security* Session - II Paper Presentation 1:00 - 2:00 hrs Session - III Paper Presentation 1:00 - 3:15 hrs Communication & Networking PROGRAMME AGENDA: 6* April 20.7 - DAY 2 Session - III Underwater Vehicles* 9:20 - 10:20 hrs LUNCH BREAK Key Note address by Dr. V. Masilama: Ascistant professor, CSE, IIIT DM Session - III Underwater Vehicles* 9:20 - 10:30 hrs Communication & Networking PROGRAMME AGENDA: 6* April 20.7 - DAY 2 Session - III Underwater Vehicles* 10:00 - 10:15 hrs Session - III Paper Presentation 10:15 - 12:15 hrs Signal Processing 12:15 - 1:15 hrs LUNCH BREAK Feedback from Participants Valedictory session Valedictory Address and Certificate Distribution	Inauguration of the event	9:05 - 9:15 hrs	Dr.S.Muthukumar, HoD-ECE, SVCE	
Session - I Paper Presentation Programme Agenda Session - II Paper Presentation Programme Agenda Session - III Paper Presentation Programme Agenda Session - IV Paper Presentation Programme Agenda Session - IV Paper Presentation Programme Session - IV Paper Presentation Programme Agenda Session - IV Programme Agenda Session - III Programme Agenda Sessi		09.15 - 09.20 hrs	NCSPCN '17 Proceedings release by HOD-EC and Chief Guest	
Session - II Paper Presentation 10.30 - 12.15 hrs 12:15 - 1:00 hrs Session - III I'mage security" 1:00 - 2:00 hrs Session - III Paper Presentation PROGRAMME AGENDA: 6* April 20:7 - DAY 2 Session - III Underwater Vehicles" 9:00 - 10:30 hrs Key Note address by Dr. V. Masilama Assistant professor, CSE, IIITDM PROGRAMME AGENDA: 6* April 20:7 - DAY 2 Session - III Underwater Vehicles" 9:00 - 10:30 hrs Feyn Note address by Tata Sudhakar, Scientist, NIOT 10:00 - 10:15 hrs Session - III Paper Presentation 10:15 - 12:15 hrs Signal Processing 12:15 - 1:15 hrs IUNCH BREAK Paper Presentation 1:15 - 2:30 hrs Feedback from Participants Valedictory session 2:45 - 3:00 hrs Valedictory Address and Certificate Distribution	Design Methodologies for Next Generation Custom Accelerators: High-Level Synthesis for System-level	9:20 - 10:20 hrs	Inaugural and Key Note address by Dr.Swathi Tanjore Gurumani, Visitin	
Paper Presentation 10.30 - 12.15 hrs 12:15 - 1:00 hrs LUNCH BREAK Key Note address by Dr. V. Masilama Assistant professor, CSE, IIITDM Session - III Paper Presentation 2:00 - 3:15 hrs Communication & Networking PROGRAMME AGENDA: 6* April 20.7 - DAY 2 Session - III Underwater Vehicles* 9:00 - 10:30 hrs Key Note address by Televorking Factor Note address by Televorking Key Note address by Televorking Interval of April 20.7 - DAY 2 Session - III Underwater Vehicles* 10:00 - 10:30 hrs Feed Session - III Paper Presentation 10:15 - 12:15 hrs Session - IV Paper Presentation 10:15 - 2:30 hrs Feedback from Participants Valedictory session 2:45 - 3:00 hrs Valedictory Address and Certificate Distribution		10.20 - 10.30 hrs	Tea Break	
Session - III Paper Presentation PROGRAMME AGENDA: 6* April 20:7 - DAY 2 Session - III Underwater Vehicles 900 - 10:30 hrs Session - III Paper Presentation PROGRAMME AGENDA: 6* April 20:7 - DAY 2 Session - III Underwater Vehicles 900 - 10:30 hrs Session - III Paper Presentation 10:15 - 12:15 hrs Session - IV Paper Presentation 10:15 - 2:30 hrs Feedback from Participants Valedictory session 2:45 - 3:00 hrs Valedictory Address and Certificate Distribution		10.30 - 12.15 hrs	VLSI	
Session - III Paper Presentation PROGRAMME AGENDA: 6* April 20.7 - DAY 2 Session - III Underwater Vehicles* Session - III Paper Presentation PROGRAMME AGENDA: 6* April 20.7 - DAY 2 Session - III Underwater Vehicles* Session - III Paper Presentation 10:15 - 12:15 hrs Session - IV Paper Presentation 10:15 - 2:30 hrs Feedback from Participants Valedictory session 1:00 - 2:00 hrs Session - IV Paper Presentation 1:00 - 2:00 hrs Session - IV Paper Presentation 1:00 - 2:00 hrs Session - IV Paper Presentation 1:15 - 2:30 hrs Feedback from Participants Valedictory session Valedictory Address and Certificate Distribution		12:15 - 1:00 hrs	LUNCH BREAK	
PROGRAMME AGENDA: 6th April 20.7 - DAY 2 Session - III "Underwater Vehicles" 9.00 - 10.30 hrs 10.00 - 10:15 hrs Session - III Paper Presentation 10:15 - 12:15 hrs Signal Processing 12:15 - 1:15 hrs LUNCH BREAK Paper Presentation 1:15 - 2:30 hrs Feedback from Participants Valedictory session 2:45 - 3:00 hrs Valedictory Address and Certificate Distribution		1:00 -2:00 hrs	Key Note address by Dr. V. Masilaman	
Session - III **Underwater Vehicles*** 9.00 - 10.30 hrs Tota Sudhakar, Scientist, NIOT 10.00 - 10:15 hrs Session - III Paper Presentation 10:15 - 12:15 hrs Signal Processing 12:15 - 1:15 hrs LUNCH BREAK Paper Presentation 1:15 - 2.30 hrs Feedback from Participants Valedictory session 2:45 - 3.00 hrs Valedictory Address and Certificate Distribution		2:00 - 3:15 hrs	Communication & Networking	
Paper Presentation 10:15 - 12:15 hrs Signal Processing 12:15 - 1:15 hrs LUNCH BREAK Paper Presentation 1:15 - 2:30 hrs Electronics, RF & Microwave 2:30 - 2:45 hrs Feedback from Participants Valedictory session 2:45 - 3:00 hrs Valedictory Address and Certificate Distribution	Session - III "Underwater Vehicles"	9.00 - 10.30 hrs	Key Note address by Tota Sudhakar, Scientist, NIOT	
Session - IV Paper Presentation 1:15 - 2:30 lns Electronics, RF & Microwave 2:30 - 2:45 hrs Feedback from Participants Valedictory session 2:45 - 3:00 hrs Valedictory Address and Certificate Distribution		10:15 - 12:15 hrs		
Paper Presentation 1:15 - 2:30 lirs Electronics, RF & Microwave 2:30 - 2:45 hrs Feedback from Participants Valedictory session 2:45 - 3:00 hrs Valedictory Address and Certificate Distribution		12:15 - 1:15 hrs	LUNCHEREAK	
Valedictory session 2:45 - 3:00 hrs Valedictory Address and Certificate Distribution	Paper Presentation	1:15 - 2:30 Jus		
Valedictory session 2:45 - 3:00 hrs Valedictory Address and Certificate Distribution		2:30 - 2:45 hrs	Feedback from Participants	
200 200	Fuledictory session	2:45 - 3:00 hrs	Valedictory Address and Carrifficate	
		2.00 2.001		

Coordinators
In G.A. Sathash Lumar
Ma T.J. Sepapenthla

Department of ECE organized, A Two Day National Conference on "Signal Processing, Communication and Networking" NCSPCN 17 on April 5th and 6th 2017.

The event was coordinated by Dr.G.A.Sathish Kumar, Professor and Ms.T.J.Jeyaprabha, Assistant Professor. The event was well organized by the members of the teamMs.D.Menaka, Ms.K.Srividhya, Ms.S.Kalyani, Ms.R.Kousalya, Ms.L.Anju, Ms.B.Sarala, Mr.M.K.Varadarajan, Ms.S.M.Mehzabeen and Ms.S.Reethi, Assistant Professors, Department of ECE.

The Conference was started with a prayer song and Welcome address by our beloved HOD-EC, **Dr.S.Muthukumar**. The Conference theme address was given by **Dr.G.A.Sathish Kumar**, Professor, Department of Electronics and Communication Engineering. The Conference was inaugurated with lightening of **Kuthu-Vilakku**, **Conference Proceeding release** by respected chief guest **Dr.Swathi Gurumani**, Asst. Prof., Department of Electronics, IITDM, Dr.G.A.Sathish Kumar, Professor, Dr.S.Muthukumar, Professor & HOD-EC, Ms.T.J.Jeyaprabha and Ms.B.Sarala, Assistant Professors, Department of Electronics and Communication Engineering.





Day 1: The Inaugural Key note address was given by **Dr.Swathi Gurumani**, on the topic: "Design Methodologies for Next Generation Custom Accelerators: High-Level Synthesis for System-level Solutions" in which he discussed about FPGA and custom IC design.



The inaugural address was continued by paper presentation. The session-I was in the field of VLSI and was judged by Dr.R.Anitha, HOD, Department of Information Technology and Dr.S.Malathi, Associate Professor, Department of ECE.



Next Key note talk was delivered by **Dr Masilamani V**, Assistant Professor, Department of Computer Science and Engineering, IIITDM, Kancheepuram, on the topic "*Image Security*", in which he discussed about the recent research issues in Image Processing & Security.



The paper presentation Session-II was on the field of Communication & Networking and was judged by Dr.G.Sumathi, Professor and Head-IMS, Dr.P.Jothilakshmi, Professor and Ms.G.Padmavathi, Professor, Department of ECE.



In day 2 Key note address was given by **Mr.Tata Sudhakar**, **Scientist**, **NIOT** on the topic "*Underwater Vehicles*" in which he discussed about the research on underwater survey missions such as detecting and mapping submerged wrecks, rocks.



The paper presentation Session-II was in the field of Signal Processing and was judged by Dr.N.Kumaratharan, Professor, Department of Information Technology and Dr.R.Gayathri, Associate Professor, Department of Electronics and Communication Engineering.





The paper presentation Session-IV was in the field of Electronics & RF and was judged by Dr.N.K.Mohanty, Professor, Department of Electrical and Electronics Engineering and Dr.H.Umma Habiba, Professor, Department of Electronics and Communication Engineering.



The Valedictory Address was given by Dr.K.R.Santha, Vice Principal & HOD, Department of Electrical and Electronics Engineering, followed by Certificate distribution to the participants by our Vice Principal.





Finally, the conference was concluded with the Vote of Thanks by Dr.G.A.Sathish Kumar, Professor, Department of ECE.







The event was successfully completed with our National Anthem. We like to thank all student volunteers from PG-CS, PG-AE, UG and supporting faculty.

We would like to thank **CSI** and **Ohmtronix** for sponsoring our Conference. All the papers presented in the Conference was selected for publication in the International Journal on Innovative Research in Computer and Communication Engineering **IJIRCCE**.

T. J. Jullee

Head of the Department
Electronics and Communication Engineering
Sat Vertratescore College of Engineering
Scientific Scientific College of Engineering

Sri Venkateswara College of Engineering Department of Electronics and Communication Engineering National Conference on Signal Processing, Communication and Networking - NCSPCN '17

05/04/2017 - SESSION I (VLSI)

Domain	Paper ID	Name of the author(s)	Paper title	Institution	Mail id	Signature
VLSI	II	G.Pavithra, R.Kousalya	DESIGN AND PERFORMANCE VERIFICATION OF RECONFIGURABLE FIR FILTER BASED ON SPLIT-UP DISTRIBUTED ARITHMETIC	SVCE		Quilto
VLSI	17	P.Kopperundevi, Dr.S.Muthukumar	SIMULATOR FOR MIPS PROCESSOR	SVCE	kopperundevipj@gmail. com. muthukumar@svce.ac.in	Koppening +
VLSI	19	T.Deepa, Dr.S.Muthukumar	DESIGN OF AN 8-BIT PROCESSOR	SVCE	Indirakama apromoni	T. Deg.
VLSI	113	N.Mageshwari, Dr.G.A.Sathish Kumar	DESIGN AND DEVELOPMENT OF APPROXIMATE MULTIPLIER USING URDHVA ALGORITHM	SVCE		J. Magler
VLSI	114	K.Swetha, M.Athappan	DESIGN AND ANALYSIS OF CARRY SELECT ADDER USING KOGGE STONE ADDER		swekannadasan@gmail.c om. athappan@svce.ac.in	Sareha
VLSI	117	Nadar James Francis Joseph Roy, Dr.S.R.Malathi	FOLDED ARCHITECTURE FOR ADAPTIVE DIGITAL FILTER	SVCE	james francis737@email .com, malathiraj@svce.ac.in	

	Paper	Name of the	Paper title	Institution	Mail id	Signature
Domain	ID	author(s)	VLSI Architecture of Matrix based RNS	SVCE	sreelekha2611@gmail.c om,sarala@svce.ac.in	V.M. Sceleth
VLSI	124		Backward converter	Meenakshi	harihb96@gmail.com ,	1-81
(3) VLSI	E1	M.Hari Hara Bhuvanesh, M.G. Aravind Sai	Efficient Implementation of VLSI on Competitive Neural Network	College of Engineering	mgaravindsai@gmail.co m	NIBO
VLSI	E3	Mohana	A VLSI-efficient signed magnitude comparator for { 2n-1, 2n, 2n+1-1} rns using kogge-stone adder	Meenakshi College of Engineering	Mohanaamaran22@gmai l.com	
ELEC	E7	S. Kruthikaa, N. Thendral, R. Vani, S.M. Gomathi, S.N. S. N. Sam	Pacemaker based on Duty Cycle Generation Pacemaker Converter of Fixed Pulse Width	College of Engineering		withises
ELEC	E8	D. SATHEESHWA RI, D.SNEKHA, M.SANGAVI	Design and Implementation of Digital Stethoscope with Disease Identification	College of	snekhadurairaju@gmail. com,sangavi.sangi2111 @gmail.com	o doda
RF	115	W. Jakahani S	DESIGN AND IMPLEMENTATION OF WIDEBAND ANTENNA USING DEFECTED GROUND STRUCTURE(DGS) FOR ULTRA WIDEBAND APPLICATION	SVCE	zvjlakshmisree@gmail.c	(Mar

Sri Venkateswara College of Engineering Department of Electronics and Communication Engineering National Conference on Signal Processing, Communication and Networking - NCSPCN

05/04/2017 - SESSION II (Communication & Networking)

omain	Paper ID	Name of the author(s)	Pan ov 4'41	anication &		
OMM	15	R.Mohanasundaram,	Paper title Design and Implementation of	Institution	1 Mail id	Signatu
O I I I I	15	S.Saravanan	Wideband Antenna for Satellite	SVCE	msundagam v@gmai	
NTW	116	S.DEEPIKA, G.PADMAVATHI	THROUGHPUT MAXIMIZATION FOR FULL-DUPLEX COGNITIVE RADIO NETWORKS USING ARTIFICIAL BEE COLONY (ABC) OPTIMIZATION TECHNIOLIE	N/E SVCE	despikamangaiqua gpadma@svce.ac.in	-
NTW	120	R.RAMYA, S.RADHIKA	SPECTRUM AWARE ANY-PATH ROUTING IN MULTI-HOP COGNITVE RADIO NETWORKS	SVCE	ramyaasparks@gmail.com. sradhika@svce.ac.in	Panyla &
COMM	1 121	P.Kalaivani, S.R.Balasubramanian	PAPR REDUCTION TECHNIQUES FOR OFDM SYSTEMS	SVCE	Kalaironia I. 100 C	P. KalaiVm:
COMN	M 122	NIVEDHA.P, T.J JEYAPRABHA	PERFORMANCE OF SECURED TURBO CODING SCHEME FOR LAND MOBILE CHANNELS	SVCE	iv-26020 t	Bull 1
NTW	E2	Jeswanth, V.R, M.Deepak, D.K. Albert Richard, Joseph Jeyakumar	Location Identification and Trainer Safety System Using Cognitive	Meenakshi College of	rieswonth?	but.

					Institution	Mail id	Signature
	Domain	Paper ID	Name of the author(s) G.Sugantha	Paper title Channel Estimation for Massive	MIT	suganthaguru93@gmail.co	4.202
0	COMM	E5	Consuganting.	иімо	Campus,AU Meenakshi	m	11.04.
(3)	COMM	E6	Anandha praba, V.	Bank locker security system using NFC	College of Engineering		Blea L
8	(K) NTW	E9		Backhaul performance of 5G networks using TWDM –PON	Velammal Engineering College	abiramielanchezian95@gm ail.com,alagammaisubrama nian30@gmail.com, hemababun@gmail.com, manishaselvan@gmail.com ,sujathar@velammal.edu.in	B-310
	СОММ	E11	Kommal.R, G.SUDHA	Blur Ray Under water Communication based Optical MODEM for the MEDUSA class	Sri Sairam Engineering College	sweetkiller23.sk@gmail.co m	
(3)	NTW	E12	B.Padmavathi, C.R.Naveen, S.Vishnu Priyan -	A review of 5G smart small cell networks using Hybrid beam forming	Jeppiaar Maamallan Franklitte pf College Technology		CANON
(3)	COM	M E13	Shalini.B, Vinothint M.C. VINODHINI	LiFi Technology based Networking	Sri Sairam Institute of Technology	nirmala.shalini@gmail.co m	B.Shalin
	NTV	W E19	Mr.K.Suresh kumar, T.Karthik Raj, R.Poovarasan,P.Aravi h Raj	LOCATION BASED TASK REMAINDER (TASK ALERTE		tkarthic8@gmail.com	
	COM	1M E20	S.Vijay Varadharajar	SIGNAL TRANSCIEVER IN ID	College of Engineering	vijav61695@gmail.com	No.
(2)) NT	N E27	R. Panina	finding potential customers using business an abythis in E- commen	Ventate Celley Frai	neces projections in corni	Image Type

Sri Venkateswara College of Engineering Department of Electronics and Communication Engineering National Conference on Signal Processing, Communication and Networking - NCSPCN 06/04/2017 - SESSION III (Signal Processing)

Domain	Paper ID	Name of the author(s)	Paper title	Institution	Mail id	Signature
SP	14	G.Deepika, M.Vidya	BACKGROUND NOISE REDUCTION USING FFBPNN-LM NETWORK AND ADAPTIVE FILTER	SVCE	deepika.140194@gmai l.com,mvidya@svce.ac .in	O.J.
SP	16	Lakshmi Thara. R, S.P.Sivagnana Subramanian	ORTHMIZATION OF BLEBS IN HUMAN EMBRYONIC STEM CELLS	SVCE	lakmethara@gmail.co m, spsivagnam@svec.ac.i	pulli
SP	18	J. Jose Nisha, Dr.R.Gayathri	MULTIFEATURE BIOMETRIC BASED AUTHENTICATION SYSTEM	SVCE	nishajoseph23@gmail. com, gayathricontact@gmail com	good.
SP	110	Rajalakshmi.P, L.Anju	FEATURE EXTRACTION AND SPEAKER IDENTIFICATION IN AUTOMATIC SPEAKER RECOGNITION SYSTEM	SVCE	The state of the s	Rejalaks
SP	112	K.HEMAVATHI, D.MENAKA	SEGMENTATION AND CLASSIFICATION FOR IMPROVED KARYOTYPING OF MULTISPECTRAL CHROMOSOME IMAGES	SVCE	hema k2801@gmail.co m, menaka@svce.ac.in	.H_160

	Paper	Name of the	Paper title	Institution	Mail id	Signature
Domain SP	123	Fincy Mol F, Ms.	COMPARING THE BRAIN REGIONS OF	SVCE	fincyffji@gmail.com, thaiyalnayaki@gmail.c	front
			HEALTHY CONTROLS AND ADHD MUSICAL SOURCE CLUSTERING		om	0.10
SP	100	Krishnapriya S,	AND RAGA IDENTIFICATION	SVCE	anju@svce.ac.in	and
SP	E4		The new method for blood vessel segmentation and optic disc detection	Sri Sairam Engineering College	pgladinol@gmail.com	,
SP	E14/	Dinakar. G and	Chromosome abnormality detection using k-means algorithm	Prathyusha	vanithafredrick@gmail .com	14.00
SP	E15,	Kishore T, Jayamani R	Image Forgery Detection using Blind Detection	Prathyusha Engineering College	kishoret2705@gmail.c om	1. Skeyon
E SP	E21	G.Vijaya Gomathi, A.Srinidhi	Study On Word Recognition By Lip Movement Analysis	Sri Sairam institute of technology	vijayagomathi1996@g mail.com,srinidhianan d4@gmail.com	P. Reyon 4. Gowatts
SP	E22	Anirudh Anand, M.Arun Singh,S. Jayakumar, T.Selvathai, Sanja Kumar Suman	Soldier's Position Tracking System		anirudhanand1996@g mail.com	-
) -		KAKILESHWAR (250)-) Venmathi.A. Dr.E.N. Gar		3.4cE.	akit-aj 96 (agmail .	K-Xlix:
SP	E23	Dr.E.N. Gar	R, Pred with Ant costony and support			

Dr. N. Kumaratharan

M. AJEET (250-)

R. GOWDHAM (250/-)

vector based etassification

participation

SYCE

Sri Venkateswara College of Engineering Department of Electronics and Communication Engineering National Conference on Signal Processing, Communication and Networking - NCSPCN '17 06/04/2017 - SESSION IV (Electronics, RF & Microwave)

omain	Paper ID	Name of the author(s)	Paper title			
RF	12	D.BHARATHI, DR.H.UMMA		Institution	Mail id	Signature
	12	HABIBA	Dual-Mode X-Band Bandpass Filter Using Stepped Impedance Resonator And Vias	SVCE	harbitha710@gmail.com.	DBIR
RF	13	V.Sneha, S.Senthillaian	Microwave Band Pass Filter Using EMBEDDER ELECTROMAGNETIC Band Gap Structures	SVCE	ummahabiba@svce.ac.in rv.sncha@vahoo.com,	By.
RF	111	L.Sridevi, B.Hemalatha	Frequency Reconfigurable Microstrip Antenna for Satellite Applications	SVCE	senthilrajan@svce.ac.in Sridevi eceqa@gracles hemalatha@svce.ac.in	
RF	118	C.Mohan, K.Srividya	Design Sign of a COmpact MIMO Antenna with Polarization Diversity for WiMax Application	SVCE		C. mus
RF	119	K.Jothilakshmi, Dr.P.Jothilakshmi	Design of Multilayer Microstrip Patch Antenna for Satellite Application	SVCE	kiothilakshmime@email.com, jothi@svce.ac.in	Kither
RF	126	SANJAY.P, VALLIMAYIL.S.R, SNEHA.R	RFID Based Personnel Location Tracking and Monitoring	SVCE	sanjusanjusanjayl 0@gmail.co m. vallimayilsr l 7@gmail.com, sncharaza04@gmail.com	Den
ELEC	E10	D.Satheeswari, M.Greeshma, Jessila Mohmatha, F.Edwin Louis	Electronic voting using finger print sensor and Andhar Card	Meenakshi College of Engineering	edwinlouis07@gmail.com	142
RF	E17	Priyanga Rubeni D, Kanthimathi-M		Sri SaiRam Engineering College	priva6deva@gmail.com, Kanthimathi.ece@sairam.edu.i	Py-
RF	E18	S.Priyanka, S.Brindha	Design of multiband antenna for wireless	Sri Sairam Engineering College Chennai	Privajan27@gmail.com brindha.ece@sairam.edu.in	

T. J. Julle

Head of the Department

Electronics and Communication Engineering
Sit Verkalesware College of Engineering
Stiperumentur - 602 117. Index

Duthiskyur.