



SRI VENKATESWARA COLLEGE OF ENGINEERING
(Autonomous)
TIME TABLE
Applicable for UG – IV Year

FT/GN/65D/00/21.04.17

With effect from: 22.01.2025

Revision No: 00

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
B.E Regulation: 2018

Academic Year: 2024-25
Semester: VIII

Section: C

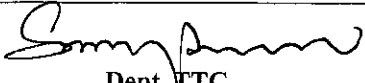
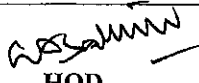

Class Room: CB506

Semester: EVEN
Block: V

DAY	1	2	3	L U N C H	4	5	B R E A K	6	7	
	8.30 – 9.20	9.20 – 10.10	10.10-11.00		11.40-12.30	12.30- 1.20		1.35-2.25	2.25 – 3.15	
MON	EC18028	EC18006	EC18018	L U N C H B R E A K	EC18006	EC18018	B R E A K	EC18028	LIB	
TUE	EC18028	EC18006	EC18018		EC18028	EC18018		EC18006	SEM	
WED	←—————				EC 18811			—————→		
THU	←—————				EC 18811			—————→		
FRI	←—————				EC 18811			—————→		

SUB CODE	SUBJECT NAME	CREDITS	NAME OF THE FACULTY	FACULTY DEPT
EC18006	Cryptography and Communication Network Security	3	Dr.G.A. Sathish Kumar	ECE
EC18018	Next Generation Networks - 5G	3	Dr. N.Kumaratharan	ECE
EC18028	Testing of VLSI	3	Dr. M. Athappan	ECE
EC 18811	Project Work	12	Mr. S.Elangovan	ECE
SEM	Seminar	-	Dr.S.R.Malathi	ECE
LIB	Library	-	Dr.R.Gayathri	ECE

FACULTY ADVISERS	ROLL NO	STUDENT REPRESENTATIVES	ROLL NO
Ms.R.Kousalya	110-126,311,312	SANJEEV R	110
Mr.N.Sathish	127-144,313,314	VIGNESH MEIYAPPAN L	154
Ms.M.Stella Mercy	145-162,315,316	SUBASREE S S	135
CLASS COMMITTEE CHAIR PERSON		Dr.S.R.Malathi	

 Dept. TTC	 HOD	 Principal
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